

**Notice of Allowability**

Application No.

10/647,306

Applicant(s)

SATO, SHINICHI

Examiner

Art Unit

Leonid Shapiro

2629

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

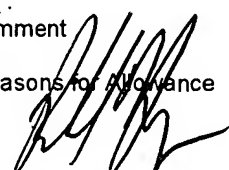
1. ☒ This communication is responsive to 08.26.03 Application.
2. ☒ The allowed claim(s) is/are 1-15.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some\* c) ☐ None of the:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
- \* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date 8-26-03
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

  
RICHARD HJERPE  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2600

***Examiner's Amendment***

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Claim 4: on Line 1, after the word "wherein" delete words "the first" and insert the following --- third ---.

Authorization for this examiner's amendment was given in a telephone interview with Attorney Adam Volentine on 04.20.06.

***Allowable Subject Matter***

2. Claims 1-15 allowed.

3. The following is an examiner's statement of reasons for allowance:

None of the cited references in combination with other limitations of claim teaches or suggests a device for driving a dot matrix display panel comprising: a plurality of switches for selectively connecting the first terminals to the second terminal, enabling a measurement device connected to the second terminal to measure electrical parameters at the first terminals individually, as disclosed in claim 1. The closest art (Nara et al., Koyama and Taniguchi) disclose related methods, either singularly or in combination fail to anticipate or suggest these limitations obvious.

None of the cited references in combination with other limitations of claim teaches or suggests a device for driving a dot matrix display panel comprising: a

plurality of first switches for selectively connecting the constant-current sources to the first terminals responsive to respective data signals, thereby supplying current to the data signal lines in the dot matrix display panel; a second terminal for connection to test apparatus; and a plurality of second switches for selectively coupling the first terminals to the second terminal, thereby enabling the test apparatus to measure the current supplied from the constant-current sources to different ones of the first terminals individually, as disclosed in claim 6.

The closest art (Nara et al., Koyama and Taniguchi) disclose related methods, either singularly or in combination fail to anticipate or suggest these limitations obvious.

None of the cited references in combination with other limitations of claim teaches or suggests a device for driving a dot matrix display panel comprising: a plurality of first switches for selectively connecting the constant-current sources to the first terminals responsive to respective data signals, thereby supplying current to the data signal lines in the dot matrix display panel; a second terminal for connection to test apparatus; a plurality of third terminals for connection to different scanning signal lines in the dot matrix display panel; a plurality of second switches for selectively connecting each the third terminals to two different potentials; and a plurality of third switches for selectively connecting the third terminals to the second terminal, thereby enabling the test apparatus to measure voltage drops in the second switches., as disclosed in claim 12. The closest art (Nara et al., Koyama and Taniguchi) disclose related methods, either singularly or in combination fail to anticipate or suggest these limitations obvious.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

***Telephone Inquire***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Leonid Shapiro whose telephone number is 571-272-7683. The examiner can normally be reached on 8 a.m. to 5 p.m..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Hjerpe can be reached on 571-272-7691. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

LS  
04.19.06



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